

## General Description

The MY3401 is the high cell density trenched P-CH MOSFET, which provide excellent  $R_{DS(ON)}$  and efficiency for most of the small power switching and load switch applications.

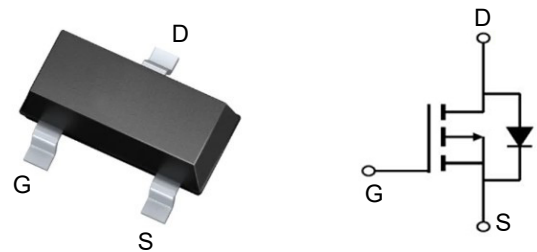


## Features

$V_{DSS}$	-20	V
$I_D$	-2.8	A
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$ )	75	$m\Omega$
$R_{DS(ON)}$ (at $V_{GS} = -2.5V$ )	90	$m\Omega$

## Application

- Green Device Available
- Super Low Gate Charge
- Excellent  $CdV/dt$  effect decline



## Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
MY3401	SOT-23	A19T	3000

## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise noted)

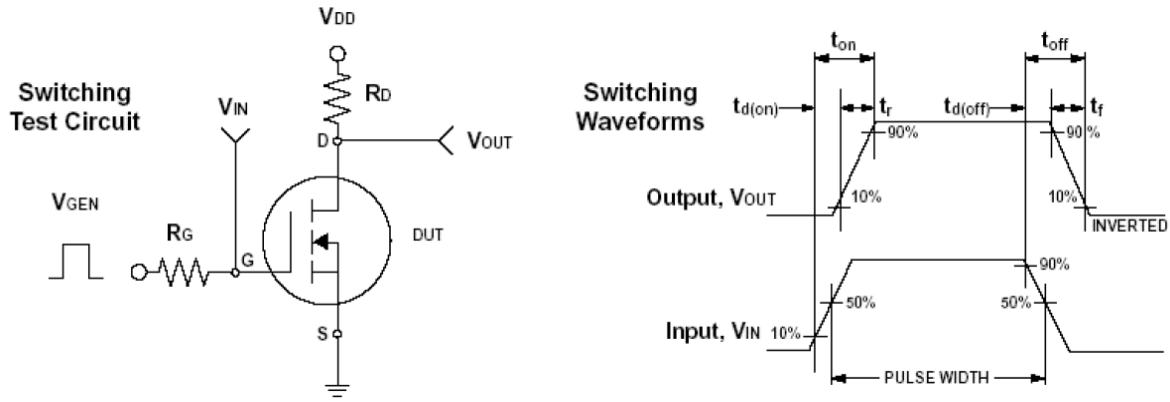
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Drain Current <sup>a</sup>	$I_D$	$T_C=25^\circ\text{C}$	-2.8
		$T_C=70^\circ\text{C}$	-2
Drain Current – Pulsed <sup>a</sup>	$I_{DM}$	-11.2	A
A Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	1.56	W
Power Dissipation – Derate above $25^\circ\text{C}$		0.013	$W/^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55 ~ +150	$^\circ\text{C}$
Operating Junction Temperature Range	$T_J$	-55 ~ +150	$^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient1	$R_{\theta JA}$	100	$^\circ\text{C/W}$

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	---	-20	---	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V	---	---	1	μA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	---	---	±100	nA
<b>On Characteristics <sup>a</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	-0.3	-0.6	-1.0	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3.0A	---	75	88	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.0A	---	90	99	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =1.0A	---	80	95	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =2A	---	4.5	---	S
<b>Drain-Source Diode Characteristics <sup>a</sup></b>						
Continuous Source Current	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-3	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A	---	---	-1.3	V
<b>Dynamic Characteristics <sup>b</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, F=1MHz	---	352	455	pF
Output Capacitance	C <sub>oss</sub>		---	63	88	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	52	67	
<b>Switching Characteristics <sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	5.7	8	nC
Gate-Source Charge	Q <sub>gs</sub>		---	0.7	1	
Gate-Drain Charge	Q <sub>gd</sub>		---	1.8	3	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =10V, V <sub>GS</sub> =4.5V, R <sub>G</sub> =25Ω I <sub>D</sub> =1A	---	2.8	6	ns
Rise Time	T <sub>r</sub>		---	8.4	16	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	18.2	36	
Fall Time	T <sub>f</sub>		---	5.7	10	

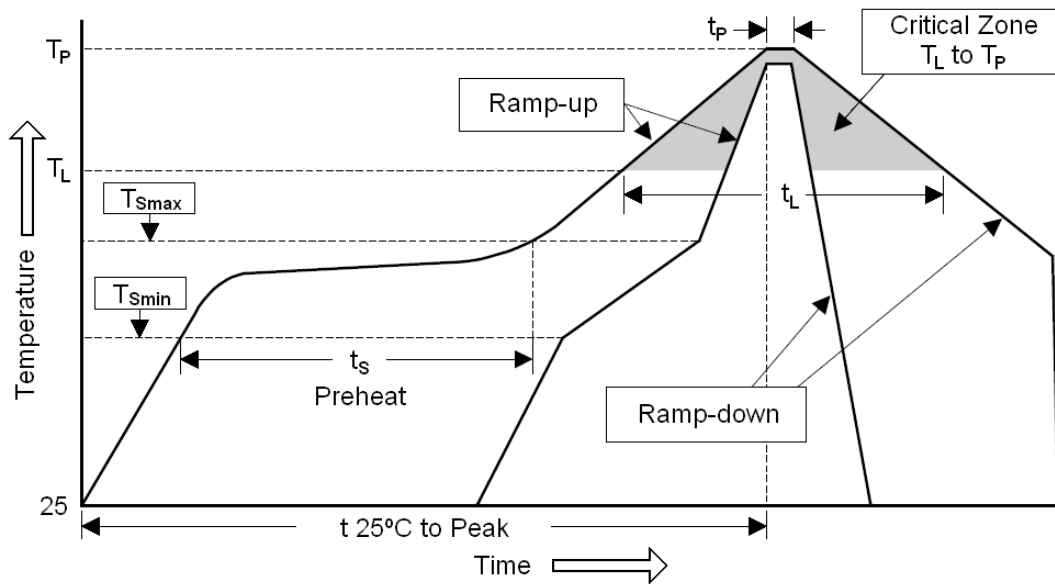
Notes: a. Repetitive Rating: Pulsed width limited by maximum junction temperature.  
 b. Pulse test: pulse width ≤ 300us, duty cycle ≤ 2%. Essential independent of operating temperature.  
 c. Guaranteed by design, not subject to production testing.

**Switching Time Test Circuit and Waveforms**



**Soldering Methods For Products**

1. Storage environment : Temperature= $10^{\circ}\text{C}$ ~ $35^{\circ}\text{C}$ , Humidity= $65\% \pm 15\%$
2. Reflow soldering of surface mount devices



**Figure : Temperature Profile**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T <sub>L</sub> to T <sub>P</sub> )	< 3°C/sec	< 3°C/sec
Preheat		
- Temperature Min (T <sub>Smin</sub> )	100°C	100°C
- Temperature Max (T <sub>Smax</sub> )	150°C	200°C
- Time (Min to Max) (t <sub>s</sub> )	60 ~ 120 sec	60 ~ 180 sec
T <sub>Smax</sub> to T <sub>L</sub>		
- Ramp-up rate	< 3°C/sec	< 3°C/sec
Time maintained above:		
- Temperature (T <sub>L</sub> )	183°C	217°C
- Time (t <sub>L</sub> )	60 ~ 150 sec	60 ~ 150 sec
Peak Temperature (T <sub>P</sub> )	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t <sub>p</sub> )	10 ~ 30 sec	20 ~ 40 sec
Ramp-down rate	< 6°C/sec	< 6°C/sec
Time 25°C to Peak Temperature	< 6 minutes	< 8 minutes

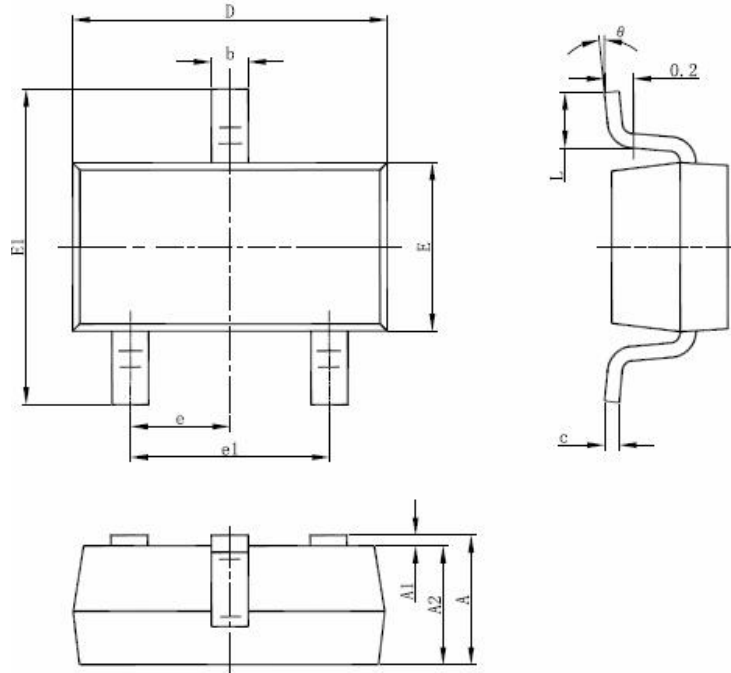
3. Flow (wave) soldering (solder dipping)

Product	Peak Temperature	Dipping Time
Pb devices	245°C ±5°C	5sec ±1sec
Pb-Free devices	260°C +0/-5°C	5sec ±1sec

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- MOS 管电路是静电敏感元器件，且对生产环境要求较严，建议在存放及生产操作时一定要避免静电干扰，经锡炉或回焊炉的温度切勿超过 260 度。

**Package Mechanical Data-SOT-23**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°